REGISTRATION FORM Name : Student/Faculty/Research Scholar/Industry Person Affiliation :.... Address :.... Phone :.......... E-mail . Accommodation Required: Yes/No Registration For : Conference/Workshop/Both Payment Mode :DD/Online Transaction Signature of the Applicant Seal/Signature of the Head Payment Details DD/ Online Transaction No :..... Date :.... Bank Name :.... Online Transaction Account Details: Account Name : ASELCOME Account No : 627302010006920 IFSC Code : UBIN0562734 Bank/ Branch : Union Bank of India /Krishnankoil

Please Mail to us

Mr. Suresh M. & Mr. Jenyfal Sampson,

Coordinators- NCVD '14 Department of ECE

Kalasalingam University

Anand Nagar, Krishnankoil - 626 126.

Tamil Nadu

Email:- ncvd14@eceklu.in

Contact No.: 8903175157, 9789436437

ORGANIZING COMMITTEE MEMBERS

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DATES TO REMEMBER

Conference:

Last date for receipt of papers : 19th August, 2014
Acceptance intimation : 26th August, 2014
Camera ready papers : 10th September, 2014

Workshop:

Last date of Registration : 10th September, 2014 Acceptance intimation : 11th September, 2014 Second National Conference on

Advancements and Future Trends in VLSI Design

NCVD'14

26th September 2014

One Day Pre Conference Workshop on

IC Design using Mentor Graphics

25th September 2014



Organized by

Department of Electronics and Communication Engineering (DST-FIST Sponsored Department) In association with IETE Student Forum(ISF)



Kalasalingam University

(Kalasalingam Academy of Research and Education) Anand Nagar, Krishnankoil-626 126. Srivilliputtur (Taluk), Virudhunagar District. Tamil Nadu, India

ABOUT THE UNIVERSITY

Kalasalingam University (formerly Arulmigu Kalasalingam College of Engineering) was established in the year 1984 by the Kalasalingam Anandam Ammal Charities. "Kalvivallal" Thiru T. Kalasalingam, veteran freedom fighter, is the founder chairman. The founding philosophy of the Institute is to "Empower Youth", for development through technology. The University offers 13 U.G. courses, 29 P.G courses and Ph.D programs in all disciplines. Our University is the first institution in India to offer the 6 years Integrated B.Tech course in Engineering for the Speech & Hearing Impaired Student. The University has signed MoU with many foreign universities, research organizations and industries with an objective of promoting research and development. Our university is accredited by National Assessment and Accreditation Council (NAAC), Bengaluru.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was instituted in the year 1993 to start a 4-year B.E. degree programme affiliated to the Madurai Kamaraj University. The first batch of students graduated from the department in the year 1997. A post graduate programme leading to M.E. degree in 'Digital Communication and Networking' was introduced in the year 2001. Consequent upon the integration of all engineering colleges under Anna University, Chennai, the affiliation both programmes were shifted to the Anna University from 2002 onwards. Based on the infrastructure and faculty strength the department was recognized as research center by Anna University in 2003 to offer M.S (by Research) and PhD programs in the area of Electronics and Communication Engineering. In the year 2004, AICTE approval was obtained for introducing a second post graduate programme in 'VLSI Design'. After a brief period of affiliation to Anna University, Chennai, the institution was finally declared Deemed-to-be University status by Govt. of India from 2007. The department have been selected as DST-FIST sponsored and sanctioned Rs.40 Lakhs towards setting up the centre for VLSI design and Image Processing.

ABOUT THE LOCATION

The University is located at the foothills of the Western Ghats in rural setting of Virudhunagar District and is catering to the needs of the downtrodden and rural youth. The University is situated at Krishnankoil, 65 kilometers south of Madurai and 10 kilometers North of Srivilliputhur, in Madurai–Shenkottai National Highway (NH208).

CONFERENCE THEME

The conference aims to bring together the National community of researchers and practitioners to discuss the latest advancements and future directions in the areas of VLSI Design, Embedded systems and technologies. The conference welcomes paper submissions from researchers, practitioners, academicians and students. The Conference welcomes papers covering exploratory research, system modeling, newer technologies and case studies. The main theme of the Conference is "VLSI Design"

WORKSHOP THEME

The objective of the program is to make the participants understand the key principles, concepts and techniques involved in IC design. The hands on training using MENTOR GRAPHICS, an industry standard CAD tool, will help the participants to design IC components to meet the given specification.

CONTENTS OF WORKSHOP

The workshop will help in acquiring the skills needed to manage your IC design project, capture and simulate your design, create the layout for your chip, use advanced interactive and automatic routing and floorplanning tools, perform DRC and LVS verification, and use extracted parasitic data in post-layout simulation. It addresses both analog and mixed-signal designs and provides everything you need to come up to speed quickly in this comprehensive IC design environment.

Topics of interest include, but not limited to

In tune with the theme, the following subtopics have been identified but not limited to, for presentation of papers and discussion at the Conference

- •VLSI circuits and systems for low power design.
- •MEMS/NEMS Design
- •Embedded Systems
- •Design, simulation and test of Digital, Analog and Mixed circuits.
- Nano-Technology
- •Nano Electronics
- •VLSI Architectures
- •Reconfigurable Computing
- •VLSI, ASIC, FPGA, MPSoC, SoC and NoC
- Circuits and Systems for Cryptography
- •CAD and EDA
- •Applications of VLSI

HOW TO REGISTER

Interested Participants from Colleges, Industry and Research Institutes are requested to fill the registration form and send the same to Post or E-Mail(ncvd14@eceklu.in). Registration fee will be accepted by online/Demand Draft drawn in favor of "ASELCOME", payable at krishnankoil along with the completed registration form On or Before . Registration fee does not include accommodation charges.

Registration Fee

Conference:

Industry/Faculty Participants : Rs. 750/-

Academicians/Research scholars &PG students: Rs.600/-

Workshop:

Industry/Faculty Participants: Rs. 1000/-

Academicians/Research scholars & PG students: Rs.750/-Registration Fee includes Proceedings, Courseware,

Working Lunch and Tea.