DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



(Kalasalingam Academy of Research and Education) (Under Sec.3 of The UGC Act, 1956) (Accredited by NAAC with B grade with a CGPA of 2.81)

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Dr. M. Pallikonda Rajasekaran

Professor and Head

MESSAGE



I am pleased to welcome you all to our Second National Conference on Advancement and Future Trends in VLSI Design (NCVD 2014) in the field of VLSI Design with the launch of conference proceedings which includes the technical papers to be presented by the authors from various parts of the Country.

Second National Conference on Advancement and Future Trends in VLSI Design (NCVD 2014) on 26th September 2014 is organized with the objective of providing a forum for the presentation of the latest developments and research findings in the area of VLSI Design, Embedded Systems, MEMS, CAD and EDA and applications of VLSI. I am grateful to all the authors and reviewers for their full hearted support. Apart from the technical paper presentation, the conference will have two plenary talks by the external experts from various institutes / universities / industries.

We are highly thankful to the Chairman, the Chancellor, the Vice-chancellor and the Registrar of Kalasalingam University for their encouragement and financial support to organize this conference. I thank the organizing Secretaries, faculty members and students who have involved in making this conference as grant success. I thank the almighty for showering his blessings on all our endeavors.

I am sure that researchers presenting papers and attending the National conference will benefit a lot by contributing and learning. I wish all of you to have a fruitful and valuable information exchange during the conference.

Convener - NCVD-2014

Date: 15.09.2014